

1 a synchronize polarizer circuit disposed between the synchronize switch and the on-  
2 screen [programming circuit] processor, for receiving the selected internal or external horizontal and  
3 vertical synchronize signals and converting the selected horizontal and vertical synchronize signals  
4 to a predefined active logic level.

5 21. (Amended) The system of Claim 20, wherein the on-screen programming circuit  
6 includes an on-screen processor that receives horizontal and vertical synchronize signals, the system  
7 further comprising a signal generator that generates internal horizontal and vertical synchronize  
8 signals.

9 22. (Amended) The system of Claim 21, further comprising:  
10 a synchronize switch coupled to receive the internal horizontal and vertical  
11 synchronize signals produced by the signal generator and external horizontal and vertical  
12 synchronize signals received from the selected computer, the synchronize switch selecting either the  
13 internal or external horizontal and vertical synchronize signals for supply to the on-screen  
14 [programming circuit] processor.

15 23. (Amended) The system of Claim 22, further comprising:  
16 a synchronize polarizer circuit disposed between the synchronize switch and the on-  
17 screen [programming circuit] processor, for receiving the selected internal or external horizontal and  
18 vertical synchronize signals and converting the selected horizontal and vertical synchronize signals  
19 to a predefined active logic level.

20 27. (Amended) The system of Claim 26, wherein the on-screen programming circuit  
21 includes an on-screen processor that receives horizontal and vertical synchronize signals, the system  
22 further comprising a signal generator that generates internal horizontal and vertical synchronize  
23 signals.

24 28. (Amended) The system of Claim 27, further comprising:  
25 a synchronize switch coupled to receive the internal horizontal and vertical

03  
1 synchronize signals produced by the signal generator and external horizontal and vertical  
2 synchronize signals received from the selected computer, the synchronize switch selecting either the  
3 internal or external horizontal and vertical synchronize signals for supply to the on-screen  
4 [programming circuit] processor.

5 29. (Amended) The system of Claim 28, further comprising:

6 a synchronize polarizer circuit disposed between the synchronize switch and the on-  
7 screen [programming circuit] processor, for receiving the selected internal or external horizontal and  
8 vertical synchronize signals and converting the selected horizontal and vertical synchronize signals  
9 to a predefined active logic level.

10 ~~233~~ (Amended) The system of Claim ~~32~~<sup>2</sup>, wherein the on-screen programming circuit  
11 includes an on-screen processor that receives horizontal and vertical synchronize signals, the system  
12 further comprising a signal generator that generates internal horizontal and vertical synchronize  
13 signals.

14 ~~334~~ (Amended) The system of Claim ~~33~~<sup>2</sup>, further comprising:

15 a synchronize switch coupled to receive the internal horizontal and vertical  
16 synchronize signals produced by the signal generator and external horizontal and vertical  
17 synchronize signals received from the selected computer, the synchronize switch selecting either the  
18 internal or external horizontal and vertical synchronize signals for supply to the on-screen  
19 [programming circuit] processor.

20 ~~435~~ (Amended) The system of Claim ~~34~~<sup>3</sup>, further comprising:

21 a synchronize polarizer circuit disposed between the synchronize switch and the on-  
22 screen [programming circuit] processor, for receiving the selected internal or external horizontal and  
23 vertical synchronize signals and converting the selected horizontal and vertical synchronize signals  
24 to a predefined active logic level.

25 Claim 37, line 1, change "36" to --35--.

1 Please add the following new claims:

2 ~~6~~--38. In a system including a workstation of the type that includes a keyboard, cursor  
3 control device and a video monitor and a plurality of individual computers, a method of transmitting  
4 keyboard, cursor control device signals and video signals between the workstation and a selected  
5 computer of the plurality of individual computers through a programmable switch, comprising:

- 6 a) receiving keyboard and cursor control device signals from the workstation;  
7 b) transmitting the keyboard and cursor control device signals from the workstation to  
8 the selected computer through the programmable switch;  
9 c) receiving keyboard, cursor control device and video signals from the selected  
10 computer;  
11 d) transmitting the keyboard, cursor control device and video signals from the  
12 selected computer to the workstation through the programmable switch;  
13 e) producing overlaid video signals for display on the video monitor of the  
14 workstation;  
15 f) receiving keyboard or cursor control device signals entered at the workstation in  
16 response to the overlaid video signals on the video monitor, and programming the switch to route the  
17 keyboard and cursor control device signals to another of the plurality of individual computers;  
18 g) repeating steps (e) and (f) whenever it is again desired to change the computer  
19 between which the keyboard and cursor control device signals from the workstation are transmitted.

20 <sup>1</sup>~~39~~. The method of Claim ~~38~~<sup>6</sup>, wherein the step of producing overlaid video signals further  
21 includes the step of:

22 analyzing the keyboard and cursor control device signals received from the  
23 workstation, wherein the overlaid video signals are produced upon receipt of a predefined keyboard  
24 or cursor control device signal.

25 ~~40~~. The method of Claim ~~38~~<sup>6</sup>, wherein the step of producing overlaid video signals further

comprises:

receiving horizontal and vertical synchronize signals from the selected computer and generating horizontal and vertical synchronize signals; and

selecting either the received horizontal or vertical synchronize signals or the generated horizontal and vertical synchronize signals for supply to an on-screen processor circuit.

<sup>9</sup>/<sub>41</sub>. The method of Claim <sup>9</sup>/<sub>40</sub>, further comprising converting the selected horizontal and vertical synchronize signals to a predefined active logic level.

<sup>10</sup>/<sub>42</sub>. In a system including a workstation of the type that includes a keyboard, cursor control device and video monitor and a plurality of remotely located computers, a method of selectively connecting the workstation to the computers for the transmission between them of keyboard and cursor control device signals, comprising:

a) producing overlaid video signals for display on the video monitor of the workstation;

b) generating a control signal in response to the video signals on the video monitor to route the keyboard and cursor control device signals from the workstation to a first computer of the plurality of remotely located computers and keyboard, cursor control and video signals from the first computer to the workstation;

c) transmitting keyboard and cursor control device signals from the workstation to the first computer, and keyboard and cursor control device and video signals from the first computer to the workstation;

d) generating a control signal in response to the overlaid video signals on the video monitor of the workstation to route the keyboard and cursor control device signals from the workstation to a second computer of the plurality of remotely located computers, and keyboard, cursor control device and video signals from the second computer to the workstation.

<sup>32</sup>/<sub>43</sub>. In a system including a workstation of the type that includes a keyboard, cursor